

VERSION WITH MARKINGS TO SHOW CHANGES MADE

The following claims have been amended in this response as follows:

- 1 1. (Amended) An apparatus comprising:
2 a first device to transmit a plurality of data
3 signals in parallel mode; and
4 a second device coupled to receive the plurality of
5 data signals from the [transmitter circuit] first device,
6 wherein the second device detects phase information
7 of each data signal against a corresponding clock signal
8 and [feed] feeds back the phase information to the first
9 device, the first device adjusts an output delay of each
10 data signal based on the phase information fed back from
11 the second device.
- 1 9. (Amended) The apparatus of claim 8 wherein the first
2 device, [upon] in response to receiving the parallel data
3 sample from the second device, compares the parallel data
4 sample to a programmed parallel data pattern.

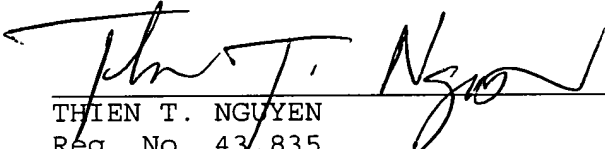
Deposit Account Authorization

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due. Furthermore, if an extension is required, then Applicants hereby request such an extension.

Respectfully submitted,

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Dated: January 7, 2003


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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on: January 7, 2003.


Pat Sullivan 1/7/03
Date